







v-MP2010MOB Mobile Video Codec Integrated Solution

The videantis v-MP2010MOB is a complete video solution integrating both stream processing and video processing into a standalone subsystem with minimum load on the host CPU. The v-MP2010MOB is the entry point of the scalable v-MP20x0MOB family with the smallest silicon cost and lowest power budget.

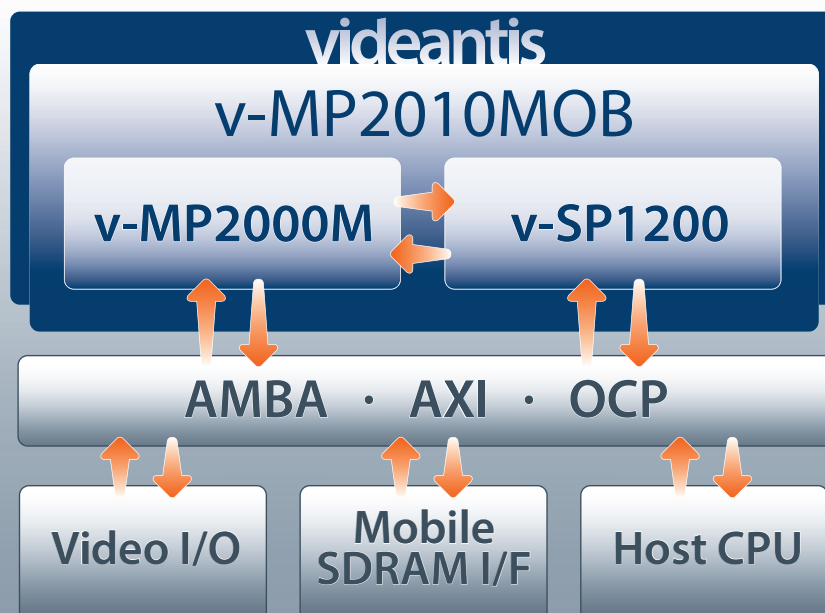
Multi-standard decoding up to D1 resolution is supported for a wide and extensible range of video standards like H.264 BP, MPEG-4 SP/ASP (DivX), WMV9/VC-1 SP/MP, and RealVideo even for high bitrates. For example, H.264 BP D1 decoding is supported up to 10Mbit/s.

Furthermore, the v-MP2010MOB is the solution with the smallest possible area footprint to support multi-standard video encoding including H.264 BP, MPEG-4 SP, and H.263 up to D1 resolution. Still image JPEG encoding and decoding is supported at a pixel rate up to 25MPixel/s. Value-add image and video processing features can be performed in parallel to decoding/encoding up to CIF resolution.

The v-MP2010MOB is available as a drop-in solution proven in real system environments and optimized to tolerate long memory access latencies of real-world systems. Extensive conformance testing has been performed to assure high product quality and full customer satisfaction. Through its field upgradeability in silicon, the v-MP2010MOB provides a future-proof solution as product lifetime can be extended through addition of further standards and value-add features simply by firmware update.

-  High quality video up to D1 mobile resolutions
-  Minimum load on host CPU
-  Ultra low power for extended battery life
-  Very small area footprint
-  Both de- & encoding on single footprint
-  Field-upgradeable codecs and

KEY ADVANTAGES





Features and Benefits

Complete solution package

- > v-MP2000M video engine + v-SP1200 multi-standard stream unit
- > Minimum load on host CPU
- > Entry point solution of scalable v-MP20x0MOB product line

Mobile video & imaging standards up to D1

- > H.264/AVC BP decode & encode, up to 10Mbit/s
- > MPEG-4 SP, H.263 decode & encode
- > MPEG-4 ASP, DivX, XviD decode
- > WMV-9, VC-1 SP/MP decode
- > RealVideo 8/9/10 decode
- > Flash Video (Sorenson Spark) decode & encode
- > JPEG still image decode & encode up to 25MPixel/s
- > Extensible to further standards on same silicon: On2 VP6, AVS, MPEG-2/-1...

Value-add image/video processing features

- > Image enhancements, rotation, scaling
- > Graphic overlays, blending, picture in picture
- > Deinterlacing, denoising, deblocking
- > Color conversion (RGB/YUV, YUV422/420)
- > Performed in parallel to decoding up to CIF resolution

Very small silicon area footprint

- > 190kgates core logic + 76kB memories
- > Target technologies: 180nm...28nm
- > 1.5mm² silicon area in 65nm technology incl. sync. bus interfaces & all memories

Low frequency requirements, ultra low power consumption for extended battery life

- > Reduced switching activity for ultra low power consumption through optimized architecture
- > Only 21mW for H.264 BP D1 decoding including FMO/ASO in 65nm technology

- Feature phone, smart phone
- Mobile Internet device (MID)
- Personal navigation device (PND)
- Digital frame
- Portable media player (PMP)
- Mobile TV, DVB-H, DMB
- Streaming video

APPLICATIONS

Easy system integration

- > SoC bus interface options: 32/64 bit, synchronous/asynchronous, AMBA AHB, AXI, OCP, others

Short time to market & future proofness

- > Reliable core, pre-verified in silicon
- > Various FPGA prototyping platforms supported
- > Field-upgradeable features and codecs by firmware download

Ready to use

- > Extensive conformance testing
- > OpenMAX IL 1.1 support for seamless integration into mobile environments

Complete solution

- > Comprehensive applications suite, fully optimized for performance and resource usage
- > Fully documented API in C source code for codecs and features
- > Example integration in application framework
- > One-stop offer including full integration support options

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